



Carnivore v2.2 MSX Cartridge RBSC 2017

BANK 1		BANK 2		BANK 3		BANK 4			
ASD0	1 (ASD0)	IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)	SLT_CS12	105 IO_LVDS42n_	IO_LVDS43p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_RFSH	21
NCSD0	2 (nCS0)	IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)	SLT_SLTSEL	106 IO_LVDS42p_	IO_LVDS43p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	RSW2	22
	3 (nCS0)	IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)	RWR	108 IO_LVDS41n_	IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	23
	4 (LVDS9p_)	IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)	SLT_CS1	110 IO_LVDS41p_	IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_INT	24
	5 (LVDS9n_)	IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)	SLT_CS2	112 IO_LVDS40n_	IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	25
	6 (LVDS8p_)	IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)	RA21	113 IO_LVDS40p_	IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	26
	7 (LVDS8n_)	IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)	RRP	114 IO_LVDS39n_	IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	27
	8 (LVDS7p_)	IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)	RA20	115 IO_LVDS39p_	IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	28
	9 (LVDS7n_)	IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)	RA08	116 IO_LVDS38n_	IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	29
	10 (LVDS6p_)	IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)	RA09	117 IO_LVDS38p_	IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	30
	11 (LVDS6n_)	IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)	RA10	118 IO_VREFB3N1	IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	31
	12 (LVDS5p_)	IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)	RA11	119 IO_DQ1R2/DQ1R11	IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	32
	13 (LVDS5n_)	IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)	RA12	120 IO_LVDS37n_	IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	33
	14 (LVDS4p_)	IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)	RA15	121 IO_LVDS37p_	IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	34
	15 (LVDS4n_)	IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)	RA14	122 IO_LVDS36n_	IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	35
	16 (LVDS3p_)	IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)	RA13	123 IO_LVDS36p_	IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	36
	17 (LVDS3n_)	IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)	RA12	124 IO_LVDS35n_	IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	37
	18 (LVDS2p_)	IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)	RA22	125 IO_LVDS35p_	IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	38
	19 (LVDS2n_)	IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)	RA01	126 IO_VREFB3N0	IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	39
	20 (LVDS1p_)	IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)	RA00	127 IO_LVDS30n_	IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	40
	21 (LVDS1n_)	IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)	RE	128 IO_LVDS30p_	IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	41
	22 (LVDS0p_)	IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)	RG	129 IO_LVDS29n_	IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	42
	23 (LVDS0n_)	IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)	RQ08	130 IO_LVDS29p_	IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	43
	24 (PLL1_OUTp)	IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)	RQ01	131 IO_LVDS28n_	IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	44
	25 (PLL1_OUTn)	IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)	RQ02	132 IO_LVDS28p_	IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	45
		IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)			IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	46
		IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)			IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	47
		IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)			IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	48
		IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)			IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	49
		IO_LVDS13p_	(DM1T/BWS#1T)/(DM1T1/BWS#1T1)			IO_LVDS44p_	(DM1R/BWS#1R1)/(DM1R1/BWS#1R1)	SLT_WAIT	50

Altera Cyclone II EP2C5Q208C8